

UNIT V**PART –A**

1. What are the three basic configurations of IC MOSFET amplifiers?
2. Describe the characteristics of CMOS CS amplifier.
3. How current sources are used to bias IC amplifiers.
4. What is IC biasing Current steering circuit using MOSFET?
5. What is the small signal behavior of an enhancement load?
6. Give the advantages of CMOS IC.
7. How does this “enhancement load” resemble a resistor?
8. What is the small signal behavior of an enhancement load?

PART – B

1. Explain the working principle of a simple MOSFET differential amplifier with an active load.
2. Derive the expression of the differential-mode voltage gain, common mode voltage gain and CMRR for a MOSFET differential amplifier.
3. Explain the working principle of a CMOS differential amplifier.
4. Explain the design of a PMOS and NMOS current sources to provide a specified bias current and output resistance.
5. Explain in detail the MOSFET amplifiers with enhancement load and depletion load.