

# SRI VIDYA COLLEGE OF ENGINEERING AND TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Conducts



*In association with ISTE*

On January 10<sup>th</sup>, 2017



**Round 1 Prelims (Time limit – 25 minutes)**

This round consists of a quiz which will be based on 8085 Microprocessor and 8051 Microcontroller.

Questionnaire will contain 20 questions

Team Size



**Round 2 Final round (Time limit – 35 minutes)**

Selected teams will proceed to final round. In this round teams are given 3 questions out of which they have to attempt any 2 programs to be debugging in 8085 Microprocessor & 8051 Microcontroller.

Last Date for Registration :05.01.2017

Mrs.R.Kanagavalli, AP/ECE  
Coordinator

Mr.P.Balaganesh,, HOD/ECE  
Convener