

**SRI VIDYA COLLEGE OF ENGINEERING & TECHNOLOGY,
VIRUDHUNAGAR**



Department of Electrical & Electronics Engineering

Cordially invite you to the

**GUEST LECTURE on
“ELECTRICAL MACHINES-I”**

20nd January, 2018

in the EEE Power System Simulation Lab

Er.R.THIRUVENGADA RAMANUJA DOSS B.E.,
Chairman, Sri Vidya group of Institutions, Virudhunagar.

&

Er.T.VENKATESH B.E.,
Vice Chairman, Sri Vidya group of Institutions, Virudhunagar.

Presides over the function

Dr.S.VIJAYARAJAN
Associate Professor / EEE Department,
Sethu Institute of Technology

Has kindly consented to be the guest and

Delivers the special address

In the presence of

Dr.S.SANKARALINGAM

Principal, Sri Vidya college of Engg. & Tech, Virudhunagar

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AGENDA

Date: 20-01-18

Time: 10.15 AM to 12.30 PM

Event : GUEST LECTURE on “ELECTRICAL MACHINES-I”

Venue : EEE Power System Simulation Lab

Prayer Song

Welcome Address: Mr.P.I.D.T.Bala Durai Kannan,
HOD / EEE

Introduction to Guest: Ms.J.R.Bindhu
AP / EEE

Felicitation Address

Dr.S.Sankaralingam, M.E, Ph.D,
Principal, SVCET, Virudhunagar.

Special Address

DR.S.VIJAYARAJAN
Associate Professor / EEE Department,
Sethu Institute of Technology.
Karriapatti.

Vote of Thanks: Mr.A.Ramaraj
AP/EEE

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